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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. BUR920040002US1
In Re Application Of: Adkisson et al.			
Serial No. 10/709672	Filing Date	Examiner	Group Art Unit
Title: LEARNING BASED LOGIC DIAGNOSIS			
<div></div> <div>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</div> <div>37 CFR 1.97(b)</div> <div>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</div> <div>37 CFR 1.97(c)</div> <div>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</div> <div><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</div> <div>OR</div> <div><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</div>			

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
BUR920040002US1

In Re Application: **Addisson et al.**

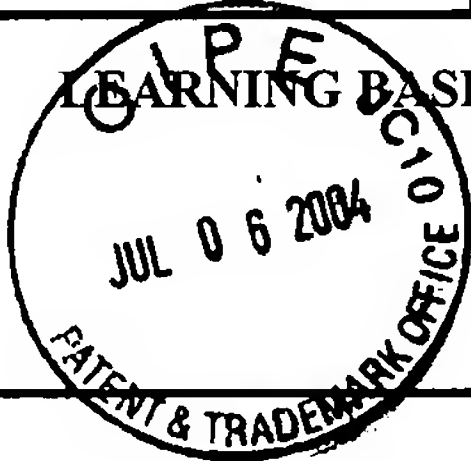
Serial No.

Filing Date

Examiner

Group Art Unit

LEARNING BASED LOGIC DIAGNOSIS



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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	-Number ⁴ -	-Kind Code ⁵ (if known)				
		DE10036118			02/14/2002	Infineon Technologies AG		
		JP2001176295			06/29/2001	NEC Corp.		
		JP2000181536			06/30/2000	Matsushita Electric Ind. Co., Ltd.		
		JP2000155156			06/06/2000	Matsushita Electric Ind. Co., Ltd.		
		JP3171754			07/25/1991	Matsushita Electric Ind. Co., Ltd.		

Examiner Signature		Date Considered	
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